

Wafer Demand Summary & Assumptions 4th Quarter 2014

January 2015
MA113-14

©Copyright Semico Research Corp. 2015. All rights reserved.

Reproduction in whole or part is prohibited without permission of Semico Research

The contents of this report represent the interpretation and analysis of statistics and information that is generally available to the public or released by responsible agencies or individuals, but is not guaranteed as to its accuracy or completeness.

Table of Contents

| | |
|--|----------|
| Table of Contents | 3 |
| Summary | 4 |
| Forecast Highlights for Q4 2014 and Major Changes from Q3 2014 and Semico's 2015 Outlook..... | 5 |
| Overall Market | 5 |
| DRAM | 5 |
| NAND | 6 |
| Microprocessors (MPU)..... | 6 |
| Microcontrollers (MCU)..... | 6 |
| Communication MOS Logic | 7 |
| Computing MOS Logic..... | 7 |
| Analog and Discrete..... | 7 |
| Optoelectronics | 8 |
| Appendix | 9 |
| Wafer Demand Forecast Methodology | 9 |
| Product Definitions | 10 |

List of Figures

| | |
|--|---|
| Figure 1. Semico Wafer Demand Methodology..... | 9 |
|--|---|

Sanitized Table from Excel

Included in this report is an excel spreadsheet providing annual wafer demand by product by technology from 2002 with a five-year forecast:

| | >1000nm | 1000nm | 800nm | 500nm | 350nm | 250nm | 180nm | 130nm | 90nm | 65nm | 45nm | 32nm | 22nm | 14nm |
|--------------------|---------|--------|-------|-------|-------|-------|-------|-------|------|------|------|------|------|------|
| DRAM | | | | | | | | | | | | | | |
| SRAM | | | | | | | | | | | | | | |
| NAND | | | | | | | | | | | | | | |
| NOR | | | | | | | | | | | | | | |
| Oth Non-Volatile | | | | | | | | | | | | | | |
| MPU | | | | | | | | | | | | | | |
| MCU | | | | | | | | | | | | | | |
| DSP | | | | | | | | | | | | | | |
| Computing | | | | | | | | | | | | | | |
| Communication | | | | | | | | | | | | | | |
| Oth MOS | | | | | | | | | | | | | | |
| Logic | | | | | | | | | | | | | | |
| Programmable Logic | | | | | | | | | | | | | | |
| Standard Cell | | | | | | | | | | | | | | |
| GateArray | | | | | | | | | | | | | | |
| Analog | | | | | | | | | | | | | | |
| Discrete | | | | | | | | | | | | | | |
| Optoelectronics | | | | | | | | | | | | | | |
| Digital | | | | | | | | | | | | | | |
| Bipolar | | | | | | | | | | | | | | |
| Total | | | | | | | | | | | | | | |