



Semico Wafer Demand: Q3 2017 Highlights

September 2017 MA112-17

©Copyright Semico Research Corp. 2017. All rights reserved.	
Reproduction in whole or part is prohibited without permission of Semico Research	
The contents of this report represent the interpretation and analysis of statistics and information that is generally available to the public or released by responsible agencies or individuals, but is not guaranteed as to its accuracy or completeness.	t

Table of Contents

Table of Contents3
Summary4
Forecast Highlights5
Overall Market
DRAM
Microprocessors (MPU)
Microcontrollers (MCU)
Communication MOS Logic
Computing MOS Logic
Analog
Discretes and Sensors
Wafer Demand by Wafer Size
·
Appendix14
Wafer Demand Forecast Methodology
Product Definitions
<u>List of Figures</u>
Figure 1. Total Semiconductor Revenue, Units & Wafer Demand, Year over Year Percent Change5
Figure 2. DRAM Revenue, Unit & Wafer Demand6
Figure 3. NAND Revenue, Unit & Wafer Demand7
Figure 4. MCU Revenue, Unit & Wafer Demand8
Figure 5. MOS Logic Communication Wafer Demand by Application9
Figure 6. Analog Revenues, Units and Wafer Demand10
Figure 7. Sensor Revenues, Units and Wafer Demand
Figure 8. Optoelectronics Revenues, Units and Wafer Demand
Figure 9. Wafer Demand by Wafer Size13
Figure 10. Semico Wafer Demand Methodology15
Figure 11. Process Technology Node Definitions