

# Semico Wafer Demand: Q2 2018 Highlights

July 2018  
MA111-18

©Copyright Semico Research Corp. 2018. All rights reserved.

Reproduction in whole or part is prohibited without permission of Semico Research

The contents of this report represent the interpretation and analysis of statistics and information that is generally available to the public or released by responsible agencies or individuals, but is not guaranteed as to its accuracy or completeness.

## **Table of Contents**

<b>Table of Contents</b> .....	<b>3</b>
<b>Summary</b> .....	<b>4</b>
<b>Forecast Highlights</b> .....	<b>5</b>
Overall Market .....	5
DRAM .....	6
NAND .....	7
Microprocessors (MPU).....	8
Microcontrollers (MCU).....	9
Communication MOS Logic .....	10
Computing MOS Logic.....	10
Analog .....	11
Discretes and Sensors .....	12
Optoelectronics .....	13
Wafer Demand Product Shifts .....	14
<b>Appendix</b> .....	<b>15</b>
Wafer Demand Forecast Methodology .....	16
Product Definitions .....	17
Process Technology Node Definitions .....	20

## **List of Figures**

Figure 1. Total Semiconductor Revenue, Units & Wafer Demand, Year over Year Percent Change.....	5
Figure 2. DRAM Revenue, Unit & Wafer Demand .....	6
Figure 3. NAND Revenue, Unit & Wafer Demand .....	7
Figure 4. MCU Revenue, Unit & Wafer Demand .....	9
Figure 5. MOS Logic Communication Wafer Demand by Application .....	10
Figure 6. Analog Revenues, Units and Wafer Demand .....	11
Figure 7. Sensor Revenues, Units and Wafer Demand .....	12
Figure 8. Optoelectronics Revenues, Units and Wafer Demand .....	13
Figure 9. Wafer Demand by Product (% of Total) .....	14
Figure 10. Semico Wafer Demand Methodology.....	17
Figure 11. Process Technology Node Definitions .....	20