

Semico Wafer Demand: Q2 2017 Highlights

June 2017
MA111-17

©Copyright Semico Research Corp. 2017. All rights reserved.

Reproduction in whole or part is prohibited without permission of Semico Research

The contents of this report represent the interpretation and analysis of statistics and information that is generally available to the public or released by responsible agencies or individuals, but is not guaranteed as to its accuracy or completeness.

Table of Contents

Table of Contents	3
Summary	4
Forecast Highlights	5
Overall Market	5
DRAM	7
NAND	8
Microprocessors (MPU).....	8
Microcontrollers (MCU).....	9
Communication MOS Logic	10
Computing MOS Logic.....	10
Analog	11
Discretes and Sensors	11
Optoelectronics	13
Process Technology Node Definition Update.....	14
Appendix	15
Wafer Demand Forecast Methodology	15
Product Definitions	16
Process Technology Node Definitions	19

List of Figures

Figure 1. Total Semiconductor Revenue, Units & Wafer Demand, Year over Year Percent Change.....	5
Figure 2. DRAM Revenue, Unit & Wafer Demand	7
Figure 3. NAND Revenue, Unit & Wafer Demand	8
Figure 4. MCU Revenue, Unit & Wafer Demand	9
Figure 5. MOS Logic Communication Wafer Demand by Application	10
Figure 6. Analog Revenues, Units and Wafer Demand	11
Figure 7. Sensor Revenues, Units and Wafer Demand	12
Figure 8. Optoelectronics Revenues, Units and Wafer Demand	13
Figure 9. Semico Process Technology Node Breakout.....	14
Figure 9. Semico Wafer Demand Methodology	16
Figure 10. Process Technology Node Definitions	19