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Arteris THE NETWORK-ON-CHIP COMPANY Cadence Constellations Di Pextreme Constellations Di Pextreme Chip Design		
SemiconductorEngineering		
7:30am – 8:30am	Registration and Continental Breakfast	
8:30am – 8:45am	Welcome & Outlook – Jim Feldhan, Semico Research Corp.	
8:45am - 9:25am	Kurt Shuler, Arteris	
9:25am - 10:10am	Chris Rowen, Cadence	
10:10am - 10:30am	Break	
10:30am – 12:00pm	Panel: IP Ecosystem Solutions for Complex Systems Moderator: Mahesh Tirupattur, AnalogBits Jason Polychronopoulos, Mentor Graphics Warren Savage, IPextreme Chris Rowen, Cadence Suk Lee, TSMC	
12:00pm – 1:30pm	Lunch – Sponsored by Tabula Steve Teig, Tabula	
	Afternoon Track 1: Semico Impact	Afternoon Track 2: Hosted by Constellations
1:30pm – 2:00pm	John Koeter, Synopsys	Warren Savage, IPextreme
2:00pm – 2:30pm	Robert Krohn, Cisco	Gerard Rauwerda, Recore Systems
2:30pm – 3:00pm	Break	
3:00pm – 4:15pm	Panel: Designing for New World Applications Moderator: Kent Shimasaki, Infinitedge Ron Moore, ARM Grant Pierce, Sonics Steve Singer, INSIDE Secure	Andrew Levy, Ridgetop Group Stephen Fairbanks, Certus Semiconductor
4:30pm – 5:30pm	John O'Neill, Skyworks Michael Johnson, Atrenta Reception – Sponsored by Cadence, IPextreme	