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PRESS RELEASE

ARM Announces Industry's First Silicon-on-Insulator Physical IP Library for IBM's New 45nm SOI Foundry

ARM in Collaboration With IBM and Soitec Ease Adoption of SOI Technology and Increase Achievement of Leading Power and Performance for the Semiconductor Industry

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CAMBRIDGE, England, November 10, 2008 /PRNewswire-FirstCall via COMTEX/ -- ARM ((LSE: ARM); ([ARMH](#): **4.67**, **-0.31**, **-6.2%**)) today announced the industry's first Silicon-on-Insulator (SOI) physical IP library including standard cell, memory and I/O libraries for IBM's fully enabled 45nm SOI foundry, also announced today. As lower power levels and increased system-on-chip (SoC) performance become more difficult to achieve with a traditional bulk CMOS process, SOI technology enables up to 30 percent better performance and 40 percent power savings at existing process nodes. The ARM(R) SOI library of physical IP is the only design platform of its kind in the industry and promises to significantly ease implementation of SOI technology and bring the benefits of this emerging process to a much wider range of semiconductor companies.

"As semiconductor vendors continue in the race toward higher speeds and lower operating power, an alternative to traditional bulk CMOS technology is critical to the continued evolution of the industry," said Joanne Itow, managing director, Semico Research Corp. "To date, two major barriers have inhibited the broad adoption of SOI as this alternative - foundry capacity and IP library availability. With today's announcements, ARM and IBM have together taken the first step toward breaking down these barriers and making SOI a viable alternative for many more applications in networking, storage, communication and consumer applications."

Tom Lantzsich, vice president of marketing for the ARM physical IP division said, "The delivery of our physical IP libraries in support of the industry's very first fully enabled SOI foundry is a testament to ARM's heritage of collaboration to remain at the forefront of technical innovation in the semiconductor industry. Our strong partnerships with industry leaders like IBM and Soitec continue to serve as the cornerstone of our business model and we look forward to working in tandem with them and others to ensure the semiconductor industry can reach new milestones in the battle for more power efficient products."

"Two years ago, Soitec and ARM announced a joint agreement supporting the development of SOI libraries for the fabless and foundry arenas, promising designers a solution to accelerate SOI adoption and design," explained Andre-Jacques Auberton-Herve, president and CEO, Soitec. "Today's announcement marks a significant milestone in delivering our promise of reducing the barriers to entry by developing the design infrastructure critical to the fabless and foundry communities."

The ARM SOI physical IP library is supported by standard synthesis and place and route implementation flows which enable customers to quickly implement products without changing design methodologies or training of employees. EDA views for Synopsys, Cadence and Magma tools are supported.

Availability

Customers can start downloading the free ARM SOI physical IP library from the ARM Web site now. For more information visit <http://www.arm.com>.

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Contact Details:

ARM PRESS OFFICE: +44-208-846-0797.

Alan Tringham,
ARM,
+44-1223-400947,
alan.tringham@arm.com ;

Erik Ploof,
ARM,
+1-425-785-1352,
erik.ploof@arm.com ;

Starlayne Meza,
Text 100 PR,
+1-415-593-8431,
starm@text100.com ;

Vicky Hayden,
Text 100 PR,
+44-208-846-0859,
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